

40G QSFP+ DAC

Part Number: LBX-DQ040Pyyy

LBX-DQ040Pyyy is a high performance passive QSFP+ DAC for 40 Gigabit Ethernet data links.

Features

- High-Density QSFP 38-PIN Connector
- Hybrid cable conforms to the Small Form Factor SFF-8436
- Maximum aggregate data rate: 41.25 Gbps (4 x 10.3125Gbit/s)
- Copper link length from 0.5~7m
- Power Supply: +3.3V
- Low power consumption: 0.02 W (typ.)
- Temperature Range: 0~70°C

Applications

- 10G/40Gigabit Ethernet
- InfiniBand SDR, DDR, QDR
- Switches, Routers, and HBAs
- Data Centers

Ordering Information

Part Number	Link Length
LBX-DQ040Pyyy	up to 7m
yyy=0P5	0.5m
yyy=001	1m
yyy=007	7m
yyy stands for length in meters	

Product Overview

Vitex **LBX-DQ040Pyyy**, QSFP+ passive cable assembly is a high performance I/O solution for 40G Ethernet applications.

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Storage Temperature	T _{STG}	-40	85	°C
Max Supply Voltage	V _{CC} T, R	-0.5	4	V

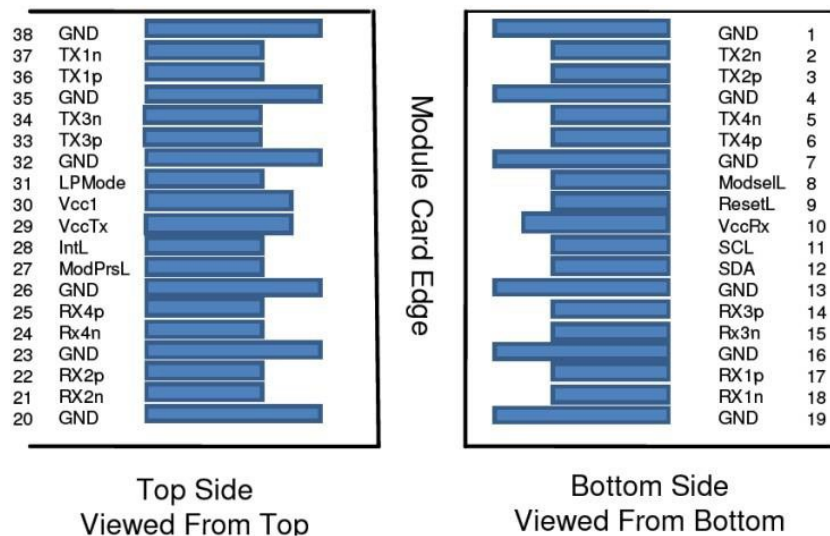
Recommended Operating Conditions

Parameter	Symbol	Min	Typical	Max	Unit
Operating Case Temperature	T _{OP}	0		70	°C
Power Supply Voltage	V _{CC}	3.14	3.3	3.47	V

Electrical Specifications

Parameter	Min	Typical	Max	Unit
Power Dissipation			0.8	W

Electrical Connector Layout

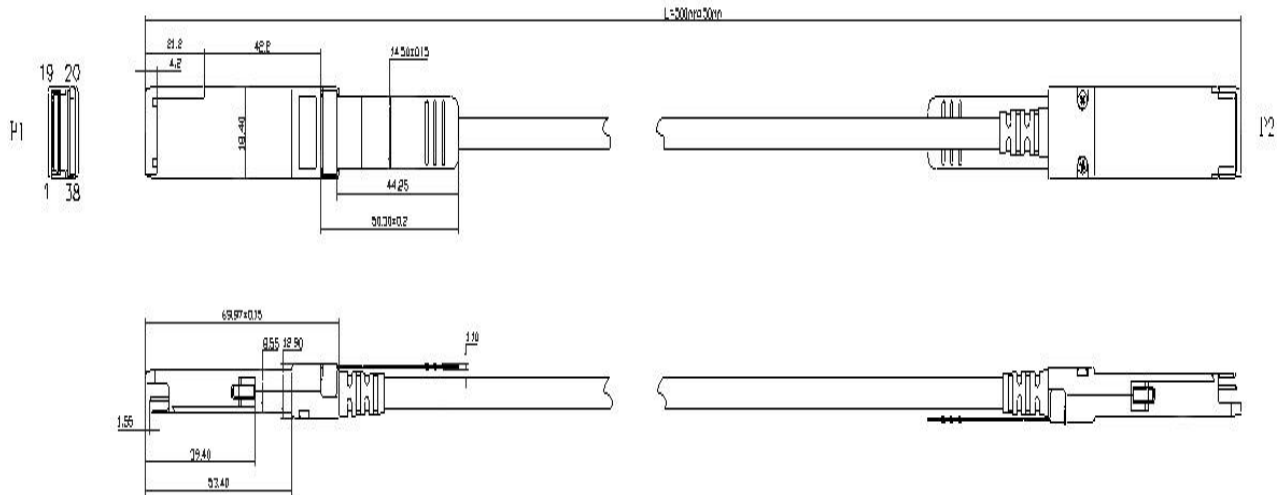


Electrical Pin Definition

Pin	Logic	Symbol	Name/Description	Note
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input	
7		GND	Ground	1
8	LVTTL-I	ModSelL	Module Select	
9	LVTTL-I	ResetL	Module Reset	
10		Vcc Rx	+3.3V Power Supply Receiver	2
11	LVC MOSI/O	SCL	2-wire serial interface clock	
12	LVC MOSI/O	SDA	2-wire serial interface data	
13		GND	Ground	1
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	
28	LVTTL-O	IntL	Interrupt	
29		VccTx	+3.3V Power supply transmitter	2
30		Vcc1	+3.3V Power supply	2
31	LVTTL-I	LPMODE	Low Power Mode	

1. GND is the symbol for signal and supply (power) common for the QSFP+ module. All are common within the QSFP+ module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal-common ground plane.
2. Vcc Rx, Vcc1 and VccTx are the receiver and transmitter power supplies and shall be applied concurrently. Vcc Rx, Vcc1 and VccTx may be internally connected within the QSFP+ module in any combination. The connector pins are each rated for a maximum current of 500mA.

Mechanical Dimensions



Length (m)	Tolerance (mm)
L ≤ 0.5	+3/-3
0.5 < L ≤ 5	+5/-5
5 < L ≤ 20	+8/-8
L > 20	+10/-10

Contact Information

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